FORM PTO-1449 (Rev. 2-32) U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

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ATTY. DOCKET NO.	SERIAL NO.
1054.024	Not yet assigned
APPLICANT	
William B. Andrews and Mou C. Lin	
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U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA	US 6,480,026 B2	11/2002	Andrews et al.	326	39	
	AB	10/397,669		Rahman et al.			03/26/03

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION YES	TRANSLATION NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AC		"Multi-Drop LVDS with Virtex-E FPGAs". By Jon Brunetti and Brian Von Herzen, Xilinx XAPP231, Version 1.0, September 23, 1999, pp. 1-11.

EXAMINER	DATE CONSIDERED
	3/15/05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

(Form PTO-1449)

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